

(LSB) Data 0 +	1	51	Data 16 +
Data 0 –	2	52	Data 16 –
Data 1 +	3	53	Data 17 +
Data 1 –	4	54	Data 17 –
Data 2 +	5	55	Data 18 +
Data 2 –	6	56	Data 18 –
Data 3 +	7	57	Data 19 +
Data 3 –	8	58	Data 19 –
Data 4 +	9	59	Data 20 +
Data 4 –	10	60	Data 20 –
Data 5 +	11	61	Data 21 +
Data 5 –	12	62	Data 21 –
Data 6 +	13	63	Data 22 +
Data 6 –	14	64	Data 22 –
Data 7 +	15	65	Data 23 +
Data 7 –	16	66	Data 23 –
Data 8 +	17	67	Data 24 +
Data 8 –	18	68	Data 24 –
Data 9 +	19	69	Data 25 +
Data 9 –	20	70	Data 25 –
Data 10 +	21	71	Data 26 +
Data 10 –	22	72	Data 26 –
Data 11 +	23	73	Data 27 +
Data 11 –	24	74	Data 27 –
Data 12 +	25	75	Data 28 +
Data 12 –	26	76	Data 28 –
Data 13 +	27	77	Data 29 +
Data 13 –	28	78	Data 29 –
Data 14 +	29	79	Data 30 +
Data 14 –	30	80	Data 30 –
Data 15 +	31	81	Data 31 +
Data 15 –	32	82	Data 31 – (MSB)
Control 0 +	33	83	RJ (RS-232)
Control 0 –	34	84	DTR (RS-232)
Control 1 +	35	85	DSR (RS-232)
Control 1 –	36	86	RTS (RS-232)
Control 2 +	37	87	CTS (RS-232)
Control 2 –	38	88	DCD (RS-232)
Control 3 +	39	89	Master Clock 1 +
Control 3 –	40	90	Master Clock 1 –
EnableA +	41	91	Master Clock 0 +
EnableA –	42	92	Master Clock 0 –
EnableB +	43	93	SOUT (RS-232)
EnableB –	44	94	SIN (RS-232)
EnableC +	45	95	External Trigger 0
EnableC –	46	96	External Trigger 1
EnableD +	47	97	External Trigger 2
EnableD –	48	98	External Trigger 3
Pixel Clock +	49	99	GND
Pixel Clock –	50	100	GND

## DB -37

Data 8 –	1	20	Data 8 + (MSB)
Data 7 –	2	21	Data 7 +
Data 6 –	3	22	Data 6 +
Data 5 –	4	23	Data 5 +
Data 4 –	5	24	Data 4 +
Data 3 –	6	25	Data 3 +
Data 2 –	7	26	Data 2 +
Data 1 –	8	27	Data 1 + (LSB)
	9	28	
	10	29	
	11	30	
	12	31	
GND	13	32	GND
VAL –	14	33	VAL + (Dot validation)
	15	34	
SLL –	16	35	SLL + (Line Start)
GND	17	36	5 V
5 V	18	37	
X Level	19		

## DB - 9

	1	6	
	2	7	
Offset +	3	8	Offset –
Gain +	4	9	Gain –
Ext. Clock +	5		Ext. Clock –

IMAQ Pin #	Signal	Thomson DB37 Pin #	IMAQ Pin #	Signal	Thomson DB9 Pin #
1	Data 0 +	27	37	Offset +	3
2	Data 0 –	8	38	Offset –	7
3	Data 1 +	26	35	Gain +	4
4	Data 1 –	7	36	Gain –	8
5	Data 2 +	25	33	Ext. Clock +	5
6	Data 2 –	6	34	Ext. Clock –	9
7	Data 3 +	24			
8	Data 3 –	5			
9	Data 4 +	23			
10	Data 4 –	4			
11	Data 5 +	22			
12	Data 5 –	3			
13	Data 6 +	21			
14	Data 6 –	2			
15	Data 7 +	20			
16	Data 7 –	1			
49	Pixel Clock +	33 (VAL +)			
50	Pixel Clock –	14 (VAL –)			
(Enable A +)	41	Line Clock +	35 (SLL +)		
(Enable A –)	42	Line Clock –	16 (SLL –)		
99	GND	13			
100	GND	17			