



# Electric Vehicle - Powertrain Test

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# EV Trends Are Challenging Test Organizations

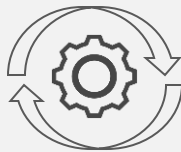
## TRENDS



Race To  
Market



Government  
Mandates



Rapid System  
Iteration

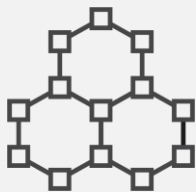


Multi-Domain  
Modeling



Emerging  
Technology

## TESTING IMPACTS



Increasing  
Complexity



Changing  
Requirements











Unrealistic  
Schedules



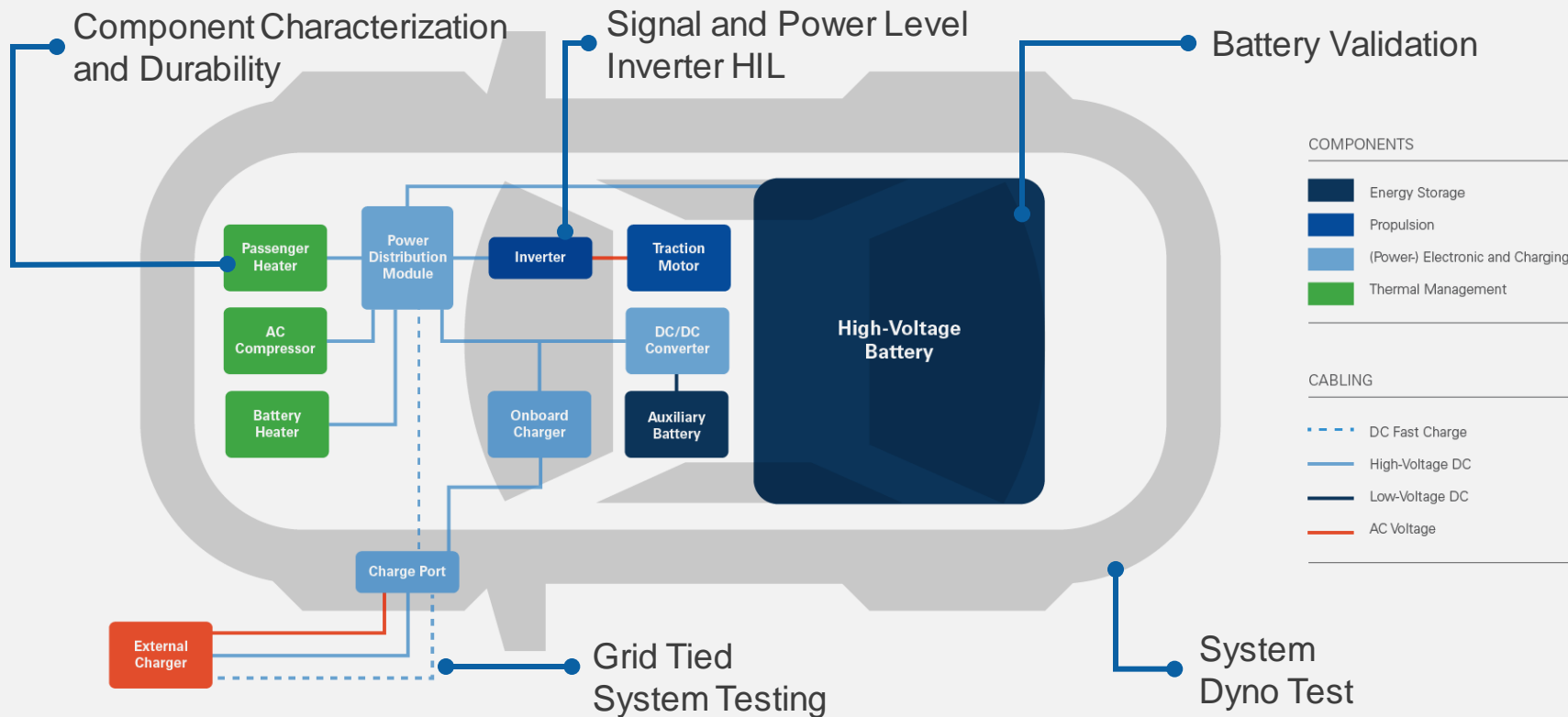
Ultra-High Speed  
Simulation

# Testing Earlier Requires More Simulation

	xCU Test 	Engine testbed 	Transmission testbed 	E-Motor testbed 	Battery testbed 	Powertrain testbed 	Vehicle CD testbed 	Road test 
VALIDATION								Maneuvers
								Chassis
							Wheels	Wheels
					Battery	Battery	Battery	Battery
				E-Motor		E-Motor	E-Motor	E-Motor
			Transmission			Transmission	Transmission	Transmission
		IC engine				IC engine	IC engine	IC engine
	xCU	ECU	TCU	xCU	BMS	xCU	xCU	xCU
SIMULATION	IC engine		IC engine	IC engine	IC engine			
	Transmission	Transmission		Transmission	Transmission			
	E-Motor	E-Motor	E-Motor		E-Motor			
	Battery	Battery	Battery	Battery				
	Wheels	Wheels	Wheels	Wheels	Wheels	Wheels		
	Chassis	Chassis	Chassis	Chassis	Chassis	Chassis	Chassis	
	Maneuvers	Maneuvers	Maneuvers	Maneuvers	Maneuvers	Maneuvers	Maneuvers	

Source: *Powertrain Instrumentation and Test Systems*,  
Michael Paulw eber and Klaus Lebert, Springer Publishing

# What Does NI Help You Test?



# EV Test System Functional Building Blocks

Control DUT

Make Measurements

Analyze & Share Data

Manage Systems



VeriStand™



LabVIEW™



DIAdem™



SystemLink™



Power Electronics



Application Specific Tester



CompactRIO



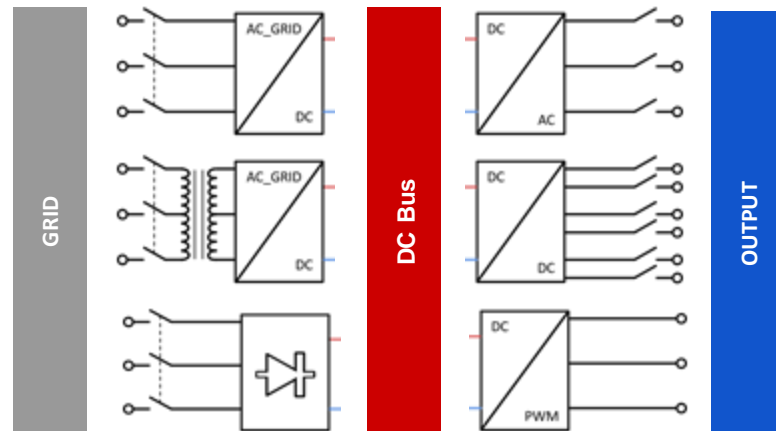
PXI



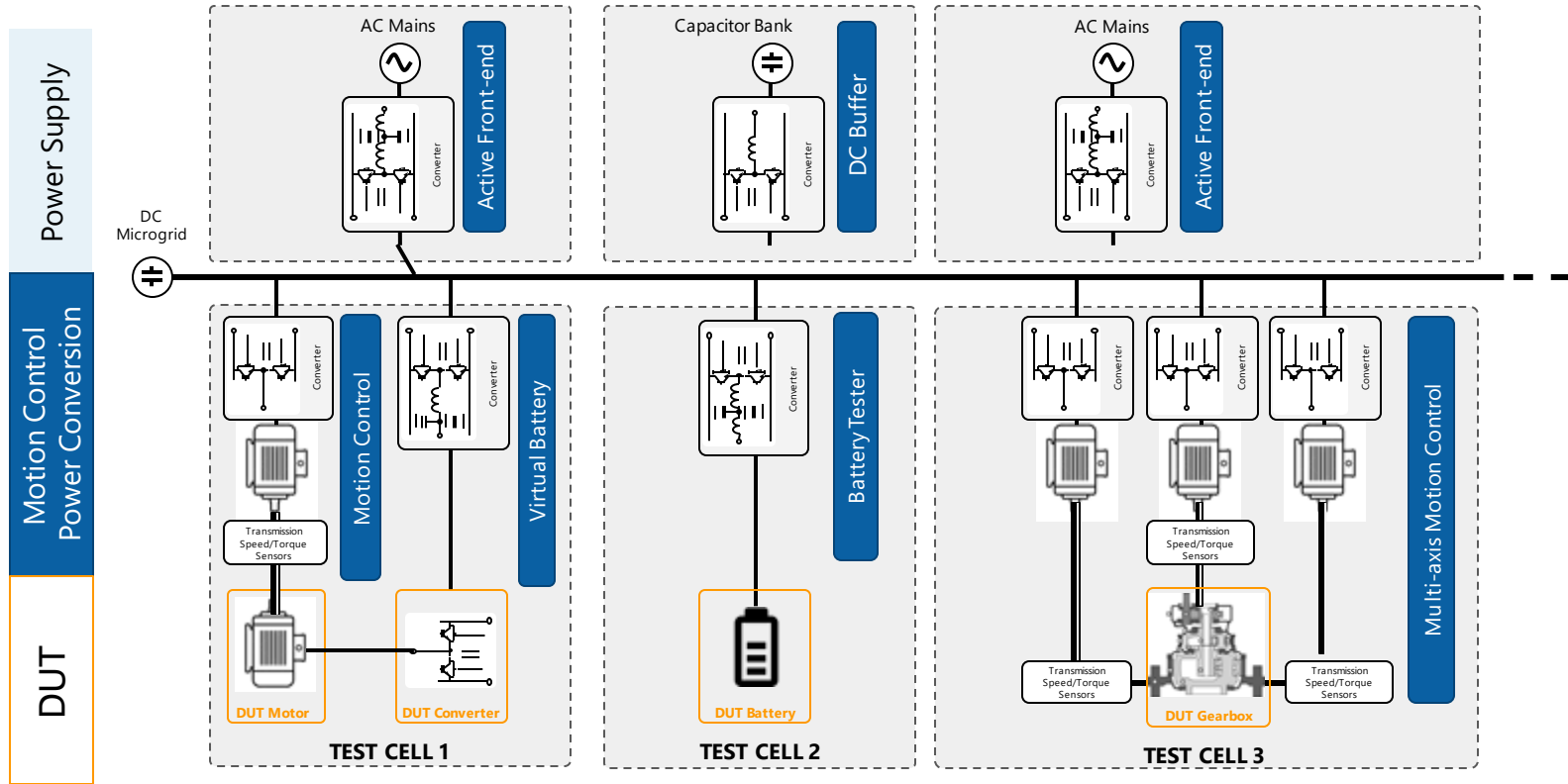
SLSC

# Power Electronics

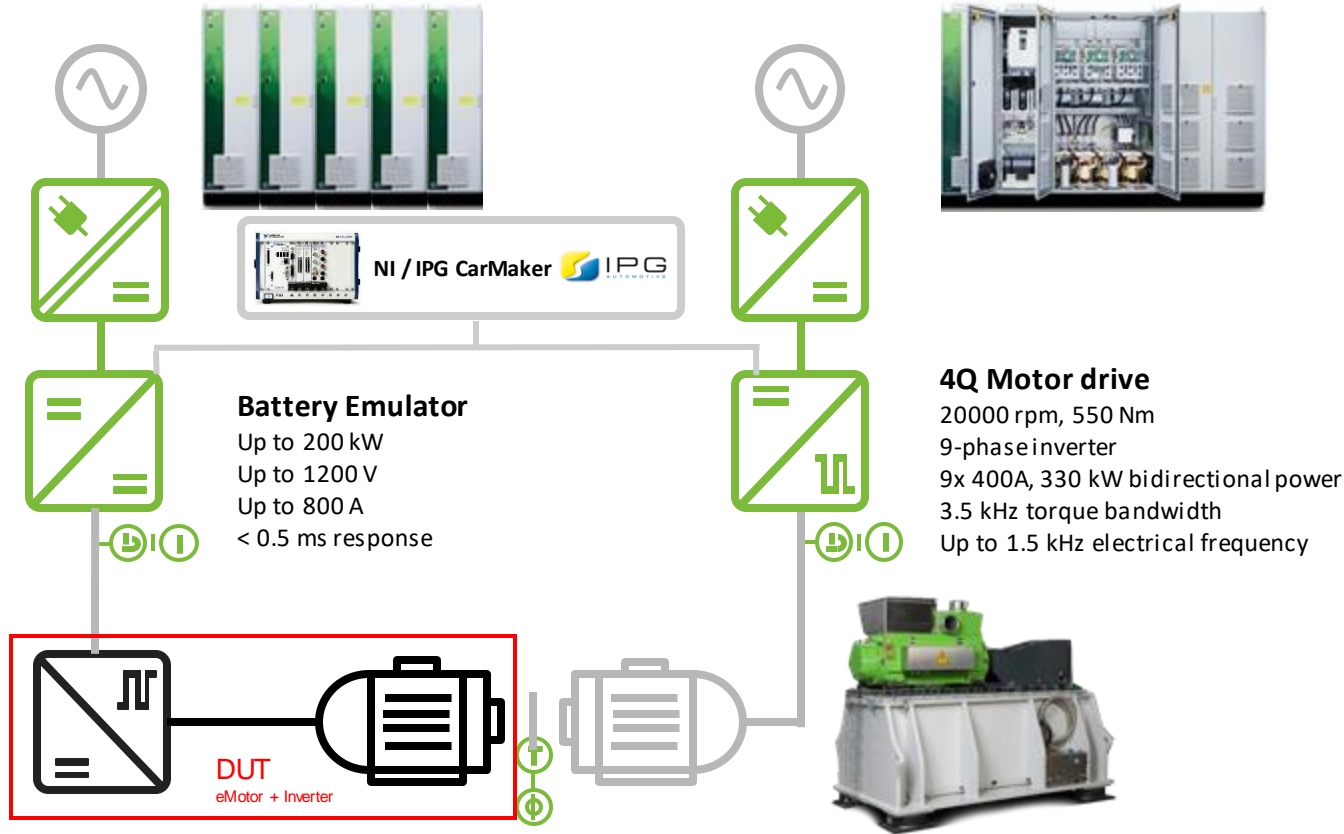
- Flexible multi-channel power conversion systems for AC and DC
- 15 kW up to 1 MW



# Test Infrastructure Example



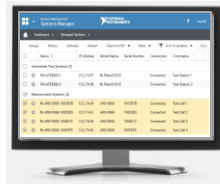
# Case Study - High Speed Dyno with Battery Emulator





# Battery Pack/Module Validation Test - Overview

- Systems consists of
  - Measurement & Control HW
  - Battery Cycler
  - Battery Test Application SW
  - System & Data Mgmt. SW



NI SystemLink

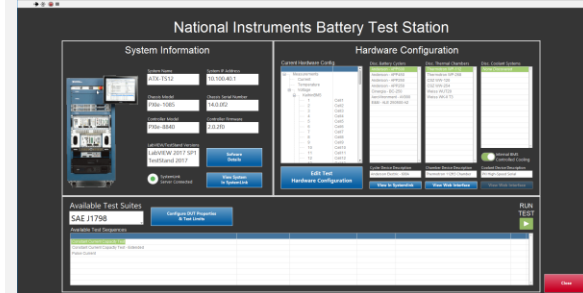
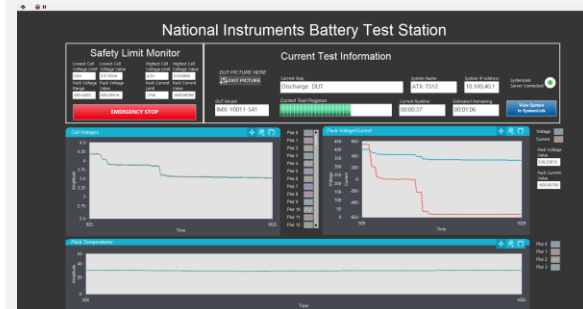


Test Automation  
Software



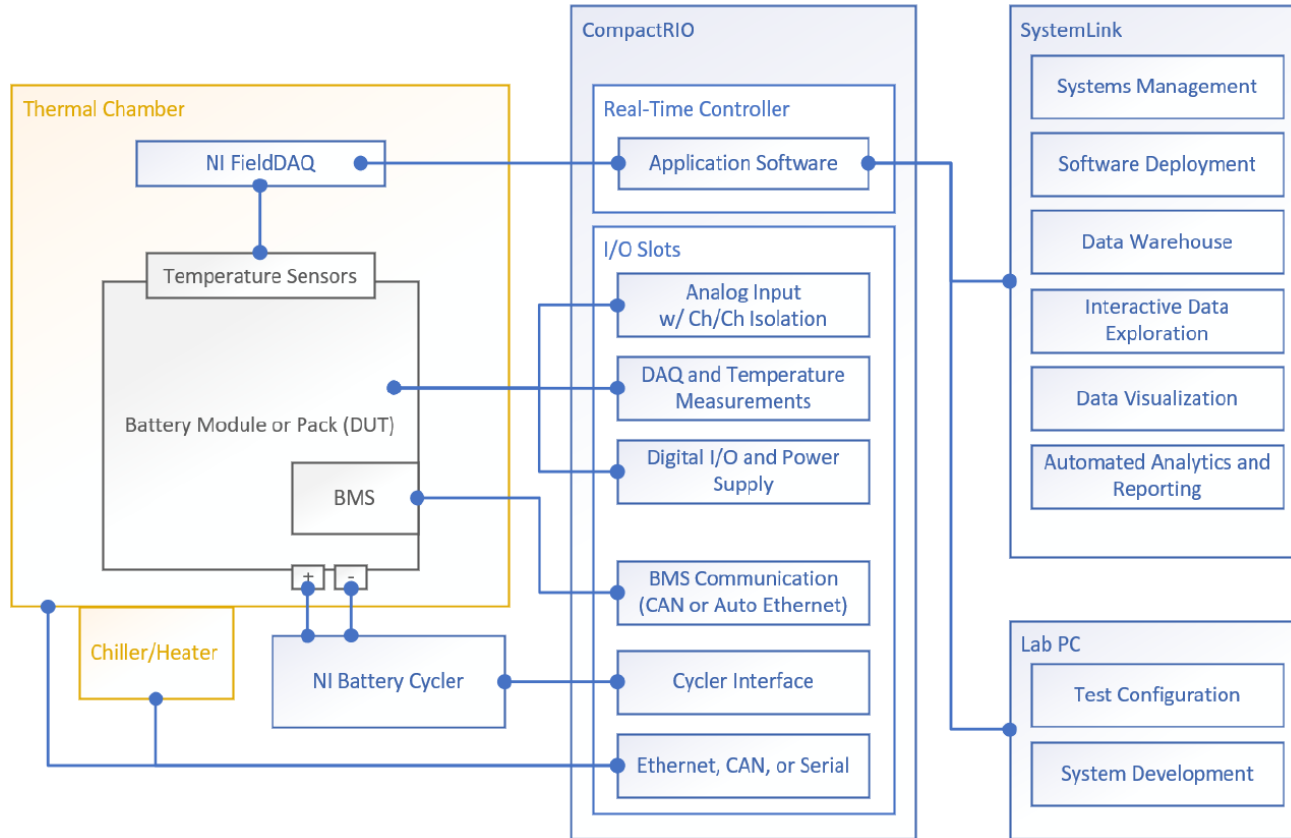
# Battery Test Software

- Real-time engine for headless, reliable execution of test sequences
- Hardware Abstraction Layer with flexible configuration to adapt to changing requirements
- Built-in sequence editor for import/export and configuration of test sequences
- Integration with SystemLink for Test Monitoring and Data Management



Steps: Main Sequence		
Step	Description	Settings
Setup (S)		
Property Loader	Property Loader	Result Recording: Disabled
Pre-Test Sequence	Locals StepKeyno = False	
Load Measurement Configuration	Call: \\.\\Desktop\\Battery (U)\\Test Monitor UI\\Load Measurement Config	New Thread
Set up Measurements	Call Configure Log File in <Current File>	Pre Expression
Start Measurements	Call Start Measurements	Pre Expression, Status Expression
Wait for CVT Creation	WaitInterval(s)	
End Group		
Main (M)		
Configure DUT for Test	Call Configure DUT for Test in <Current File>	
Pre	Call Configure Current Test Sequence in <Current File>	
Configure DUT for Charging	Call Configure DUT for Charging in <Current File>	
Charge DUT	Call Charge DUT in <Current File>	
Configure DUT for Discharging	Call Configure DUT for Discharging in <Current File>	
Discharge DUT	Call Discharge DUT in <Current File>	
End	Call Shutdown Test in <Current File>	
Shutdown Test		
End Group		
Closeup (C)		
Stop Monitoring UI	Locals StepKeyno = True	Status Expression, Additional Results
Upload Logs to SystemLink		Post Expression
Stop Measurements		
End Group		

# NI Battery Test System - Block Diagram



# Key Specifications

<b>Equipment Integration</b>	Integrate any 3 <sup>rd</sup> party environmental chambers or control/measurement devices and easily manage a heterogeneous fleet of equipment
<b>Flexible Control</b>	Sequencing, alarms, fully definable profiles, and variable/PID/custom thermal setpoints and profiles
<b>Data and Systems Management</b>	Scalable enterprise ready tools for data organization and storage, interactive data exploration/visualization, custom automated reporting and analytics, SW versioning and remote deployment, etc.
<b>Charge/Discharge Mode</b>	CC/CV/CP/Waveform
<i>Voltage Range (Cycler)</i>	<i>0-1200V</i>
<i>Current Range (Cycler)</i>	<i>±1600A</i>
<i>Power Range</i>	<i>Up to 1.2MW</i>
<i>Power Regeneration</i>	<i>Recycle energy back to the DC bus or to the grid (utility)</i>
<b>High Precision Measurements</b>	Easily add any of NI's extensive I/O library to scale the test system to meet test requirements now and in the future (temperature, thermal cameras, DIO, stress/strain, vibration, etc.) for few to hundreds of I/O channels
<i>Cell Voltage</i>	<i>±10 V, 24 Bit, 1 kS/s/ch Simultaneous, 250 Vrms, CAT II, channel-to-channel isolation</i>
<i>Cell Temperature</i>	<i>J, K, T, E, N, B, R, and S thermocouple Types (24-bit, simultaneous sampling)</i>
<i>Digital Input/Output</i>	<i>30 VDC, 7 μs Sinking DI, 500 μs Sourcing DO, 60 VDC, CAT I, channel-to-earth isolation, with PWM support</i>



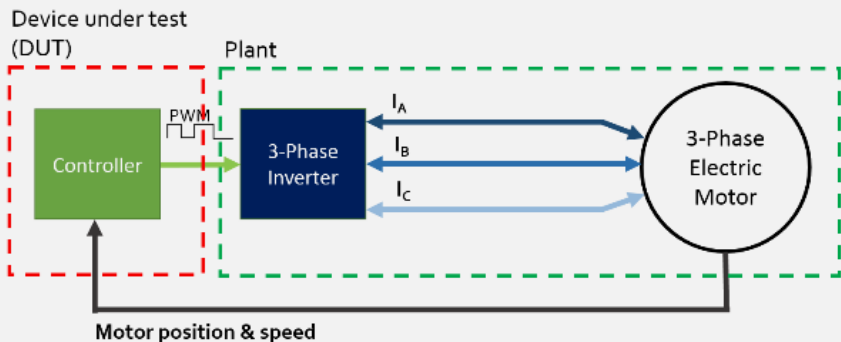
# Inverter Software Testing (signal level)

**Plant including inverter and motor are modeled FPGA:**

- Varying converter topologies and motor designs
- Fault and non-ideal behavior simulation
- Hall-effect and resolver simulation

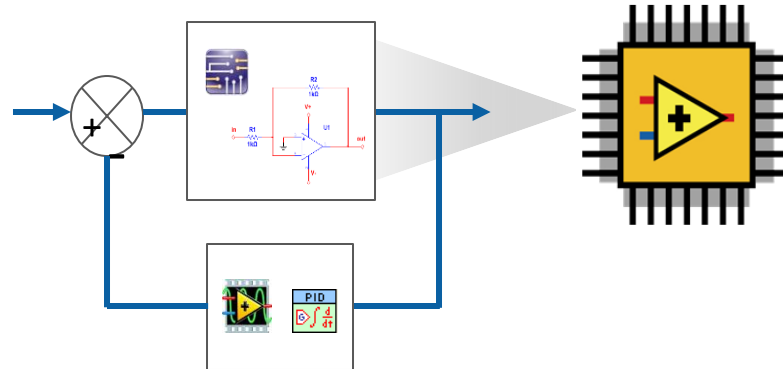
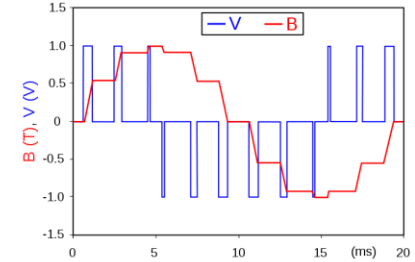
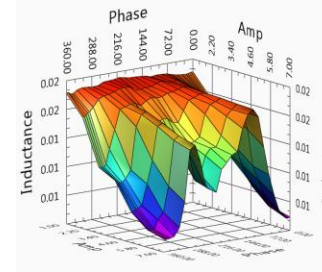
**Controller is connected to simulator via:**

- Analog/Digital I/Os
- CAN-bus
- TCP/IP-based protocols

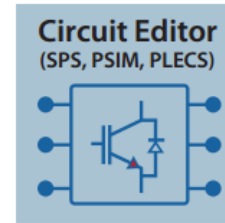
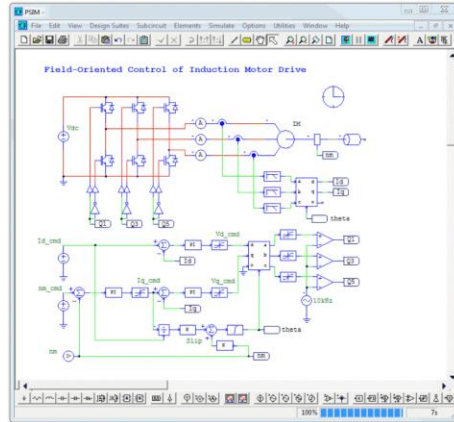


# What Makes Power Electronics HIL Different?

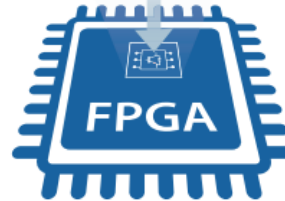
- Very fast PWM inputs to models
- Special electrical modeling tools
  - Simscape Electrical™ (formerly SimPowerSystems)
  - PLEXIM
  - PSIM
  - Multisim
- Non-linear motor models
- Power-level testing – from 500W to 25MW



# OPAL-RT – Circuit Simulation in FPGA



Automatic Model  
Generation



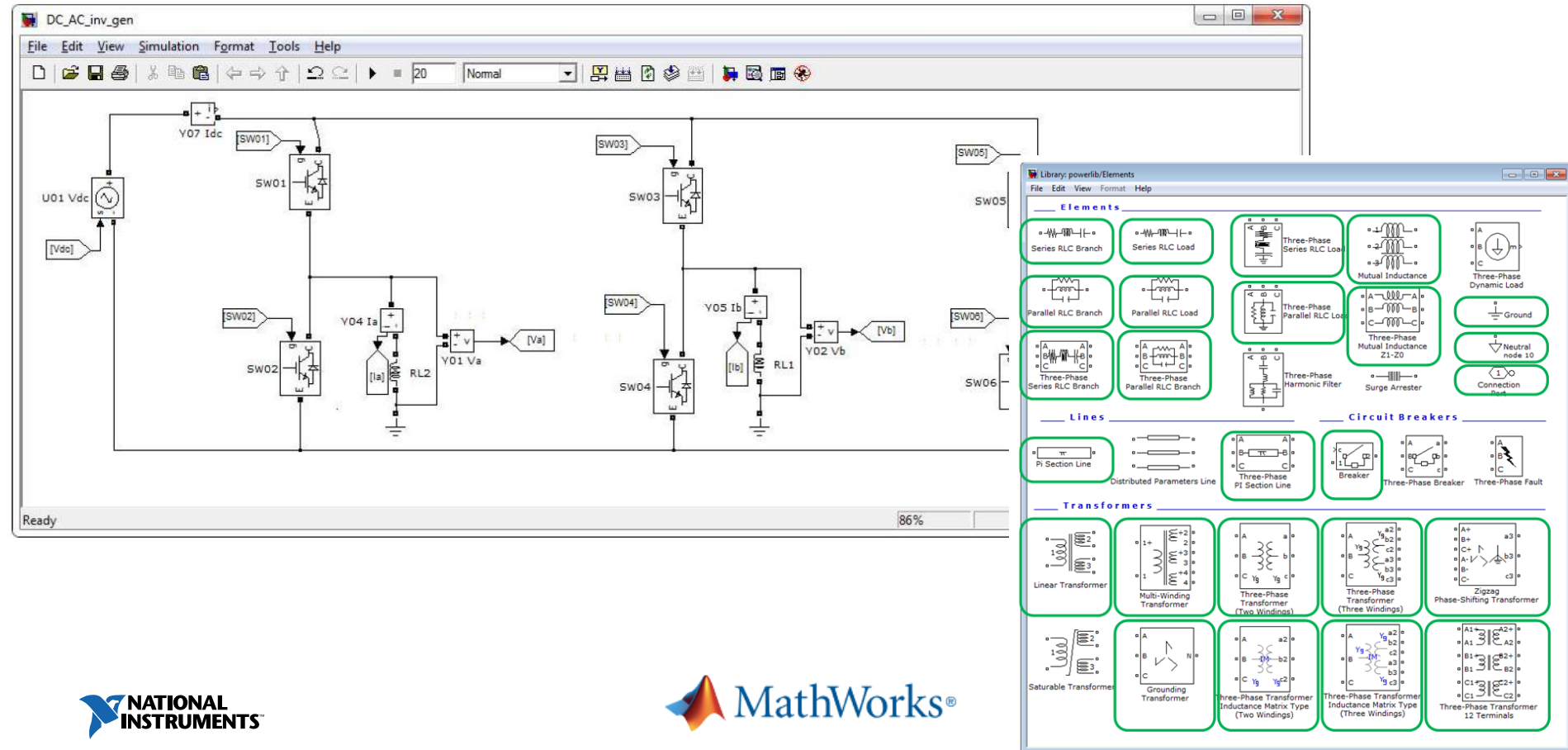
eHS

Automatic generation  
of electric circuit model:

- No mathematical modeling
- No FPGA expertise
- No VHDL programming
- No need for Xilinx Blockset or other Xilinx FPGA tools



# Example: Three-Phase Inverter in Simulink / Simscape Electrical



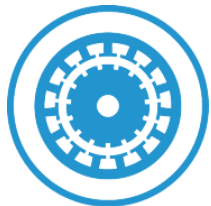
# Electric Machine Simulation



## Permanent Magnet Synchronous Machine (PMSM – IPM – BLDC- SPM)

The automotive and transportation industries comprise the core markets for PMSMs. This machine type is known for its power density (power per unit of size/weight), and its higher speed capacity. OPAL-RT's solutions provide resolver and encoder I/O interfaces and communications protocols to exchange time-accurate information and position sensor responses. The following PMSM models have been developed:

- Constant parameter model
- Variable parameter model
- Finite element analysis (FEA) model



## Induction Machine (DFG – DFM – Squirrel Cage Induction Machine)

Induction motors are widely used as industrial drives because they are self-starting, reliable and economical. They're also increasingly used with variable-frequency drives (VFDs) in variable-speed service, as well as in wind turbines, for example. OPAL-RT's solutions support various machine configurations, machine parameters that can be modified at runtime, enabling flexible test possibilities--making our simulation tools indispensable for induction machine control testing. The following IM models have been developed:

- Constant parameter mode

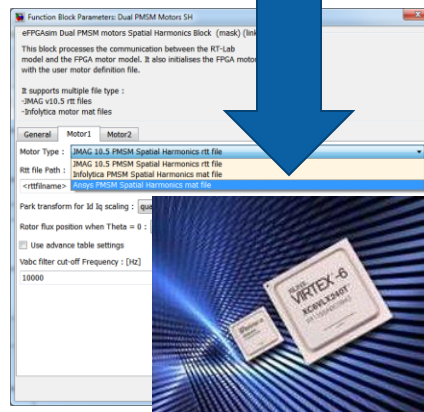
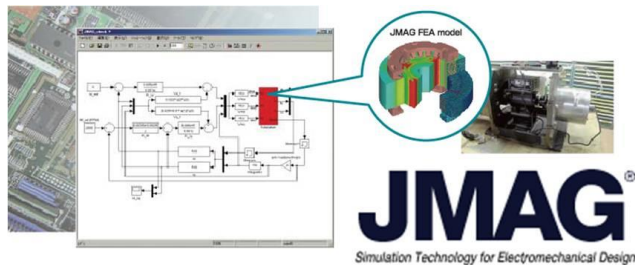
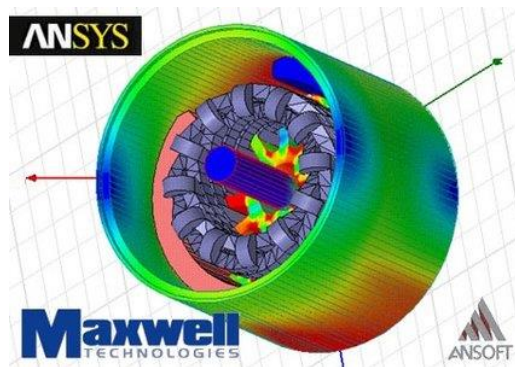


## Switch Reluctance Machine (SRM)

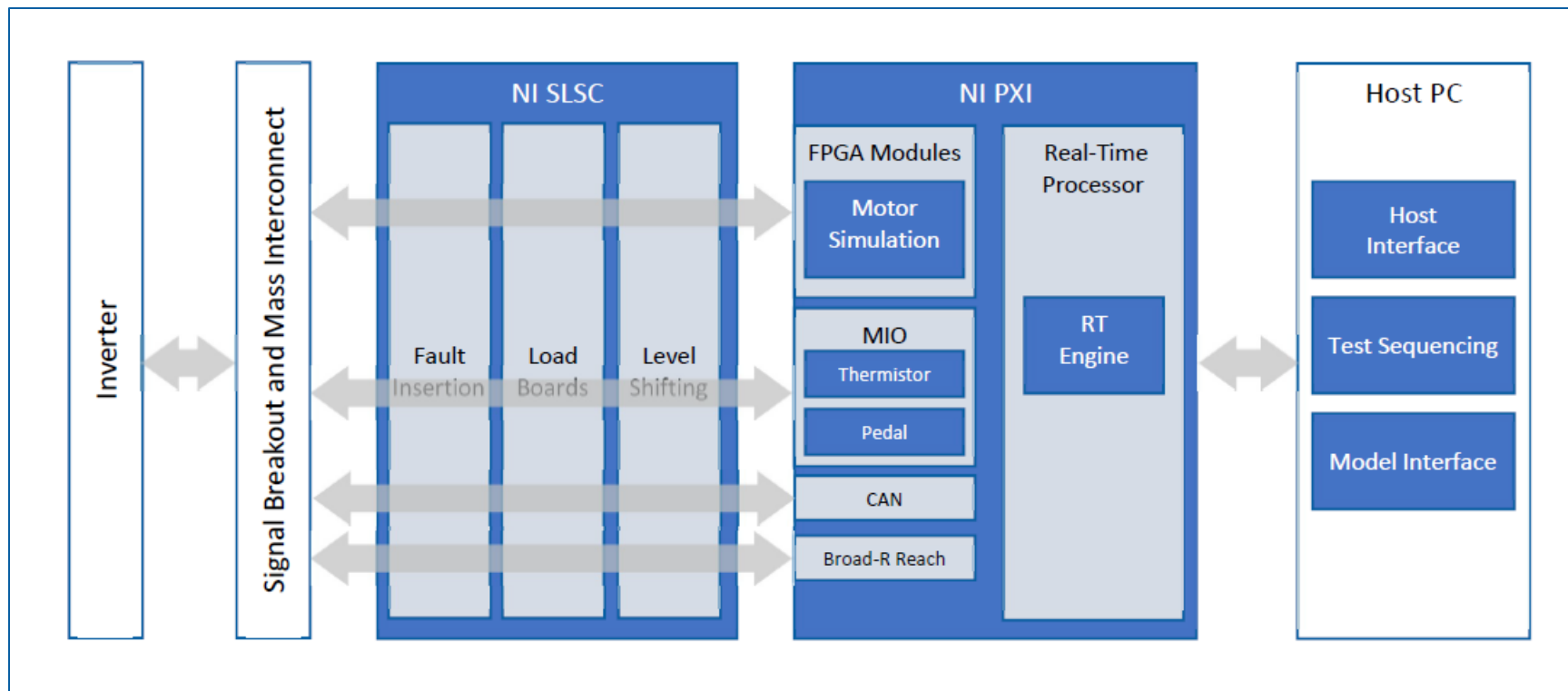
The switch reluctance machine runs with reluctance torque and delivers power to windings in the stator. SRMs are used in appliances and vehicles, are considered uncoupled, and have a complex relationship between excitation current, rotor position, and flux linkages. OPAL-RT's solutions interface easily with modeling packages for finished components, like JSOL's JMAG-Studio or Maxwell from Ansys. This improves high-precision testing and logging of the complex informational exchanges between the flux and other variables mentioned above.

- Linear model
- FEA model

# IMPORT OF SPATIAL HARMONICS MODELS (FEA)



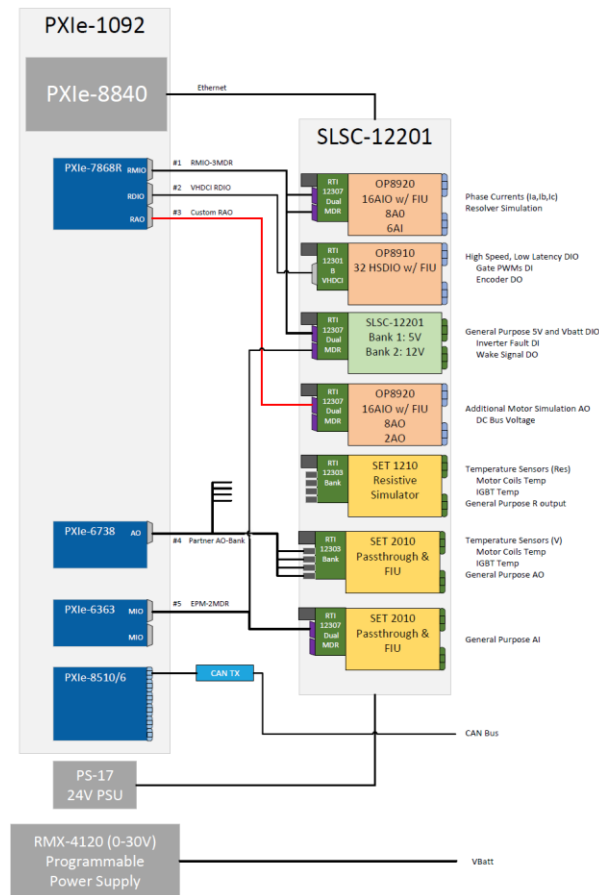
# Signal Level Inverter Validation



# Inverter HIL – Application Specific System

## Key Features and Advantages:

- Unique Accuracy and Fidelity with Analog I/O at 1MS and 16 Bit
- True closed-loop model rate including I/O as low as 1µsec
- Accredited Calibration Options
- Large FPGA (Xilinx Kintex-7 325T)
- Extensive Fault Insertion Features
- No FPGA-compile time due to eHS precompiled model engine with direct connect to Matlab Simscape Electrical and other modelling environments



# PXle-786x FPGA based I/O Board for EV HiL Applications

- Analog Input
  - 6 Analog Input Channels, 16 Bit, Differential or Single Ended Input
  - 1 MS/sec Simultaneous
  - Input Ranges: +/- 1V, 2V, 5V, 10V
  - Overvoltage Protection 42 Volt
  - Internal (to onboard references) and External Calibration
- Analog Output
  - 18 Analog Output Channels, 16 Bit
  - 1 MS/sec +/- 10V
  - Internal (to onboard references) and External Calibration
- Digital I/O
  - 16Ch at 10MHz max, 32 Ch at 80 MHz max
- FPGA: Xilinx Kintex-7 325T



## ***Fast Model Loop Rates of up to 1μsec – FPGA Architecture and Peer to Peer Streaming***

By performing the simulation calculations on the FPGA in parallel to the I/O node, there is no communication latency. Additionally, if you have to split the simulation across multiple modules, you can use peer to peer streaming transfer data directly without having to go through host memory, which further minimizes latency.

## ***Specialized Modeling Tools – Power Electronics Simulation Tools by OPAL-RT***

OPAL RT tools let you use existing design models from industry-standard power electronics modeling toolchains and deploy the models directly to FPGA for very high-speed power electronics simulation.

# NI SLSC

An open architecture for extending NI hardware with switches, loads, and signal conditioning targeted at HIL applications.

- Enables larger switches for fault insertion
- Handles small to medium loads on a simple circuit card
- Adds custom signal conditioning
- Reduces signal routing complexity

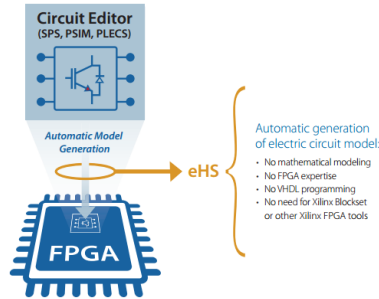


# Design Validation of Future EPS Systems

## Design for Fail Operational Redundant Systems

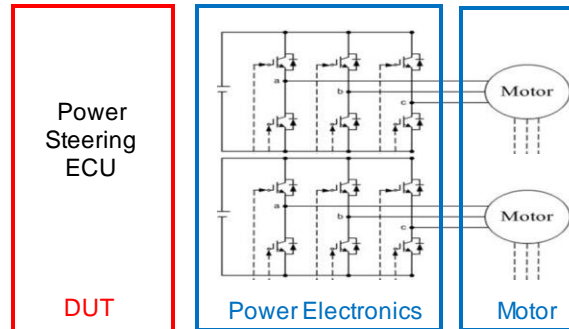
### Requirements

- Steering of future autonomous cars need to evolve from fail safe to fail operational mode and require new ways of advanced dual model based motor emulation techniques



### Solution

- Use Simulink Models to simulate Power Electronics (Inverter) stage and motor
- Fault insertion
- Early validation software enables test before power stage and motor are present







# Subaru HIL System



Custom integration with existing test tools

- Flexible test software
- Automated testing and analysis

Motor models imported from finite element analysis software

FPGA-based simulation – high speed computation

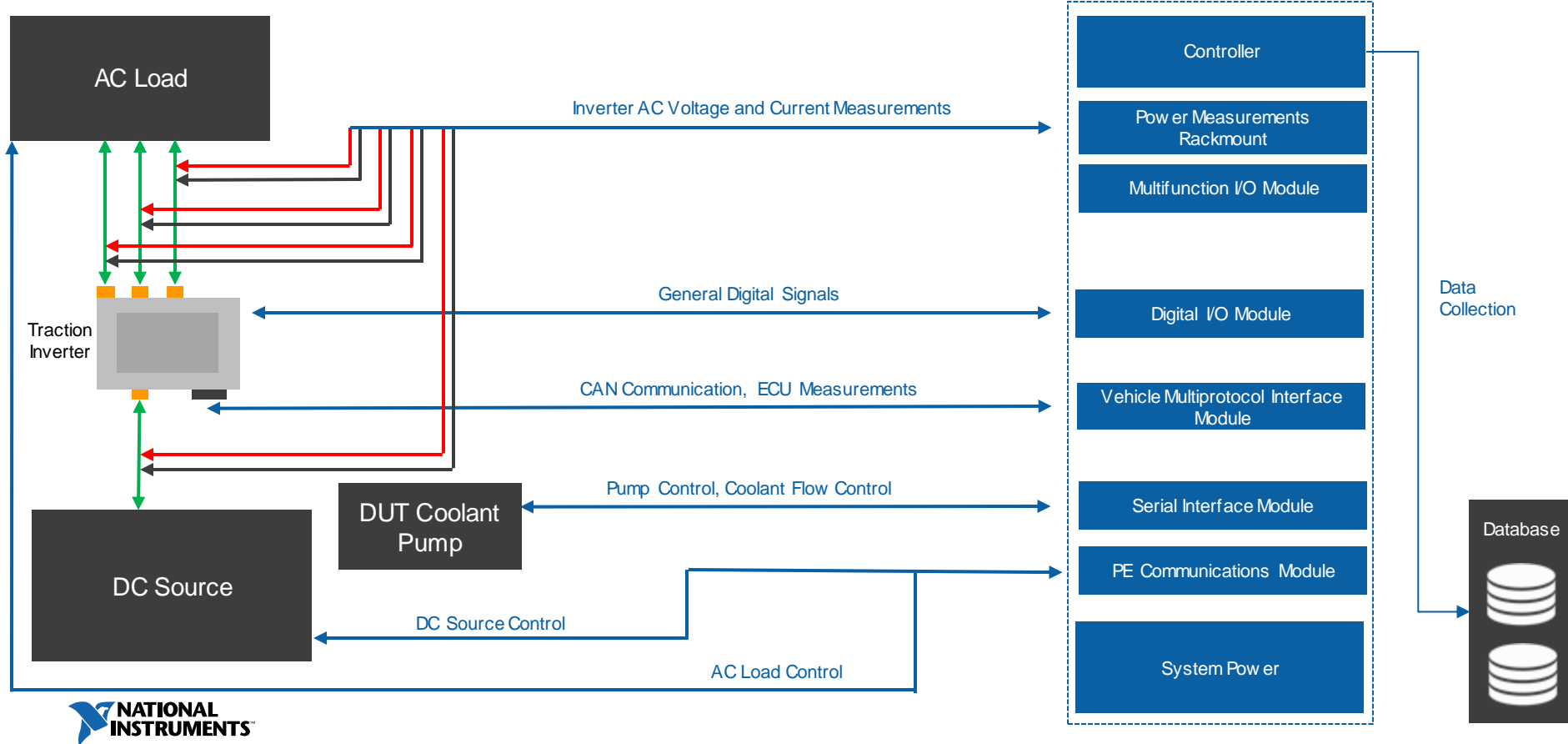
- Low IO latency
- Loop rates near 1 micro-sec

Improved test coverage

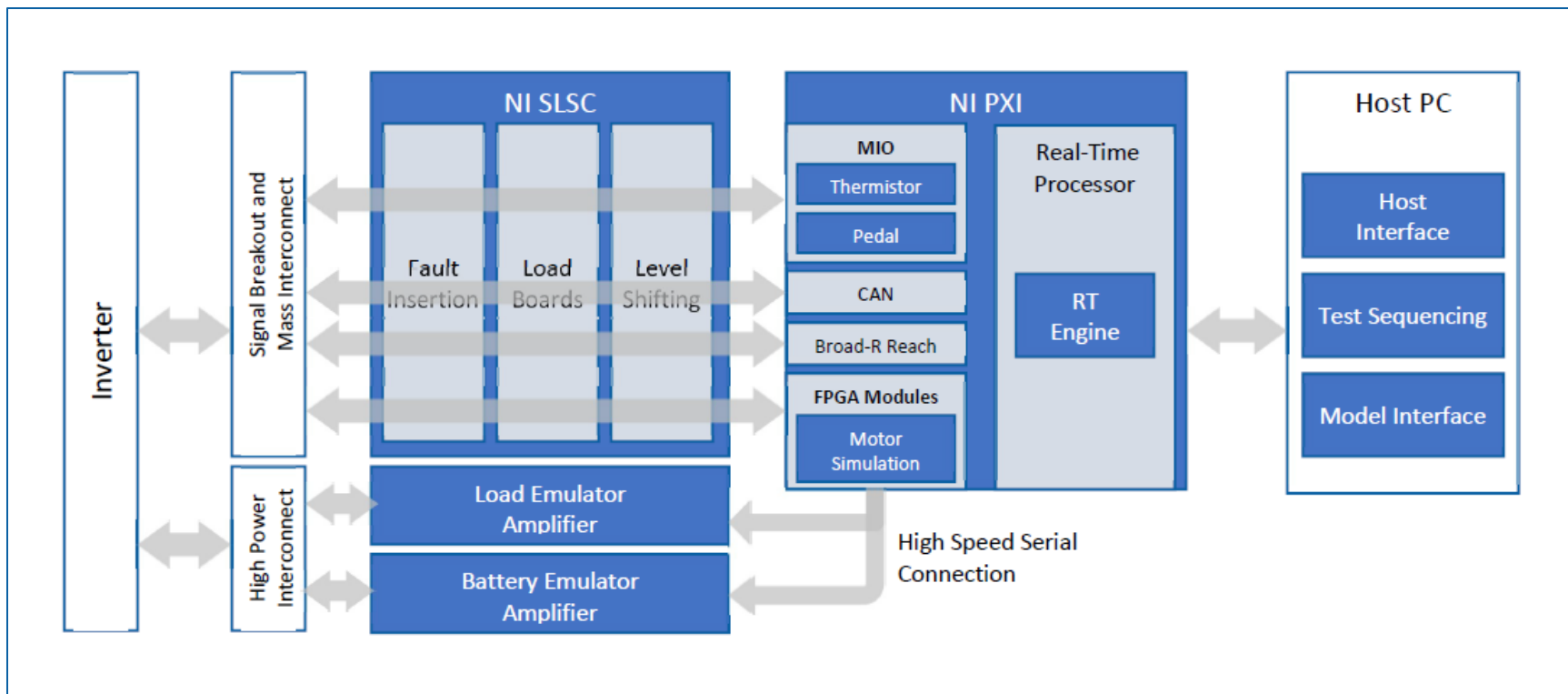
- Testing torque ripple of the electric motor
- DRAM

*By adopting FPGA-based simulation using the NI hardware and software platforms, we achieved the simulation speed and model fidelity required for verification of an electric motor ECU. We reduced test time to 1/20 of the estimated time for equivalent testing on a dynamometer.*

# Power Level Inverter V&V Test (non-dyno)

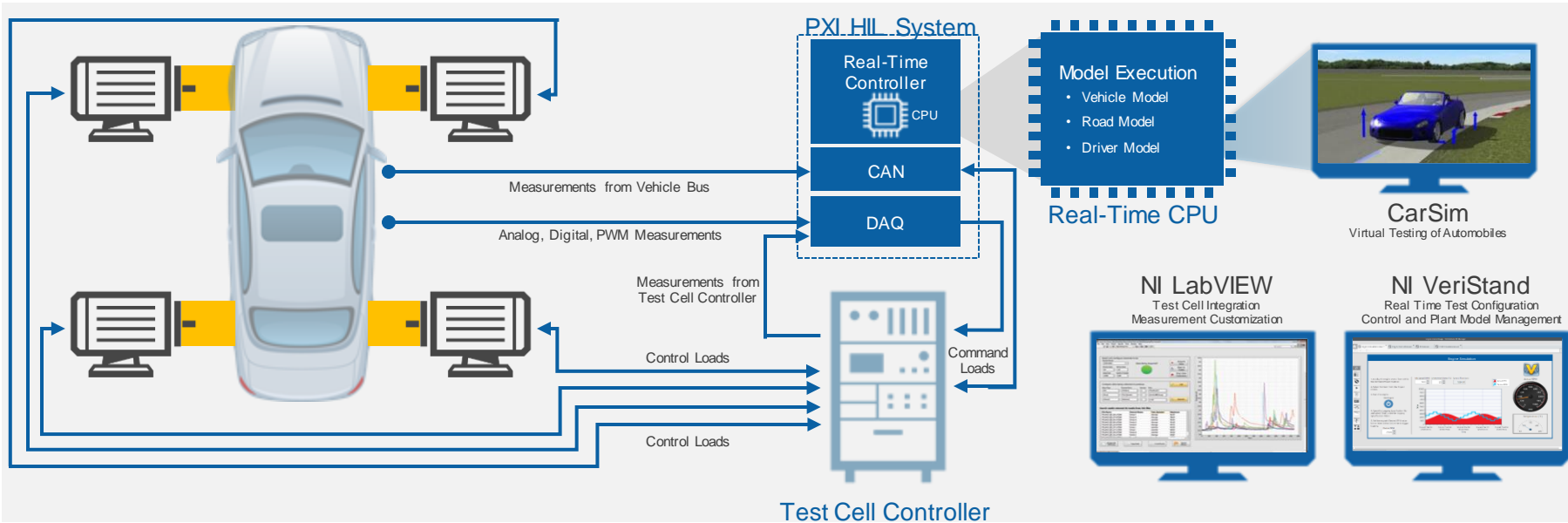


# Power Level Inverter Validation



## System Performance Test (Model-based Test Cell)

- Real-time model execution (Vehicle, Road, and Driver)
- Dedicated motors for each wheel apply loads to wheels in real time
- Malicious Tests – conditions cannot be realized in field tests
- Easy to change conditions (ex. Icy road surface with a 30% gradient → change it to 40% instantly)





Source: National Instruments Case Study – Subaru and Horiba

“By adopting NI HIL Platform for CarSim vehicle and road simulation on a Horiba dynamometer, we were able to replace over 70% of on-road testing with the simulated environment and reduced 30% of test time. We achieved 1/6 of the automated test software development time and cut 1/3 of test system capital cost using LabVIEW and PXI.”

—Mr. Daisuke Umiguchi  
Electrified Power Unit Research and Experiment Dept

# Power Measurements Conditioner

RM-26999

## Features

- High power measurement capabilities
- Four power measurement channels
- 2 kV voltage input
  - Four input ranges:  $\pm 200\text{V}$ ,  $\pm 400\text{V}$ ,  $\pm 1000\text{V}$ ,  $\pm 2000\text{V}$
- Current sensor support with options up to 2,000 A
- Single cable sensor connectivity
- Integrated sensor communication and power supply
- Up to 3.5 MS/s/ch simultaneous measurements
  - Compatible with PXIe-6356, PXIe-6366, PXIe-6376

## Software

- Getting started examples for common configurations
- RM-26999 API with NI-DAQmx







Thank you for your Attention

Joergen Etter

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