



Technologie intelligent toegepast

Waarom software niet meer de bottleneck is

Onderwerpen



- ✖ Introductie NBG
- ✖ Productontwikkelingsproces
- ✖ Hardware in the Loop (HIL)

NIDays 09

Worldwide Virtual Instrumentation Conference



NBG

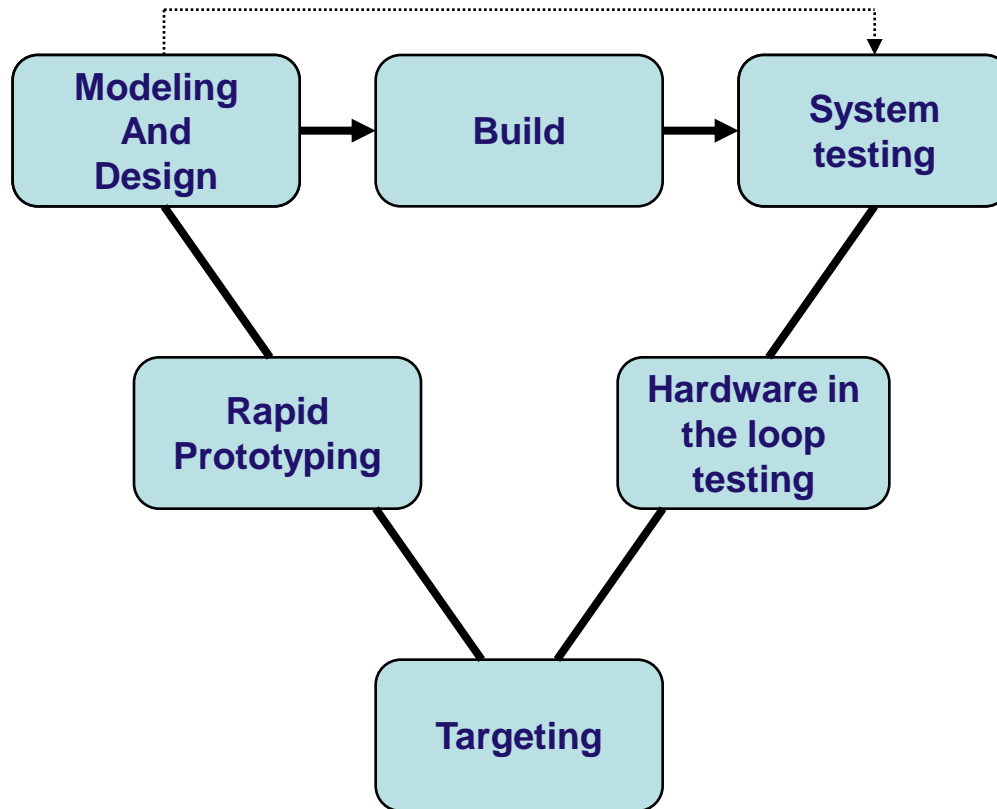
- ✖ Ontwikkeling van hardware, embedded en PC-software voor industriële en medische toepassingen

LabVIEW

- ✖ Ontwerpen/programmeren van grote en complexe applicaties
- ✖ Testsystemen
- ✖ Ruime elektronica kennis
- ✖ Turnkey projecten
- ✖ Consultancy



Productontwikkeling

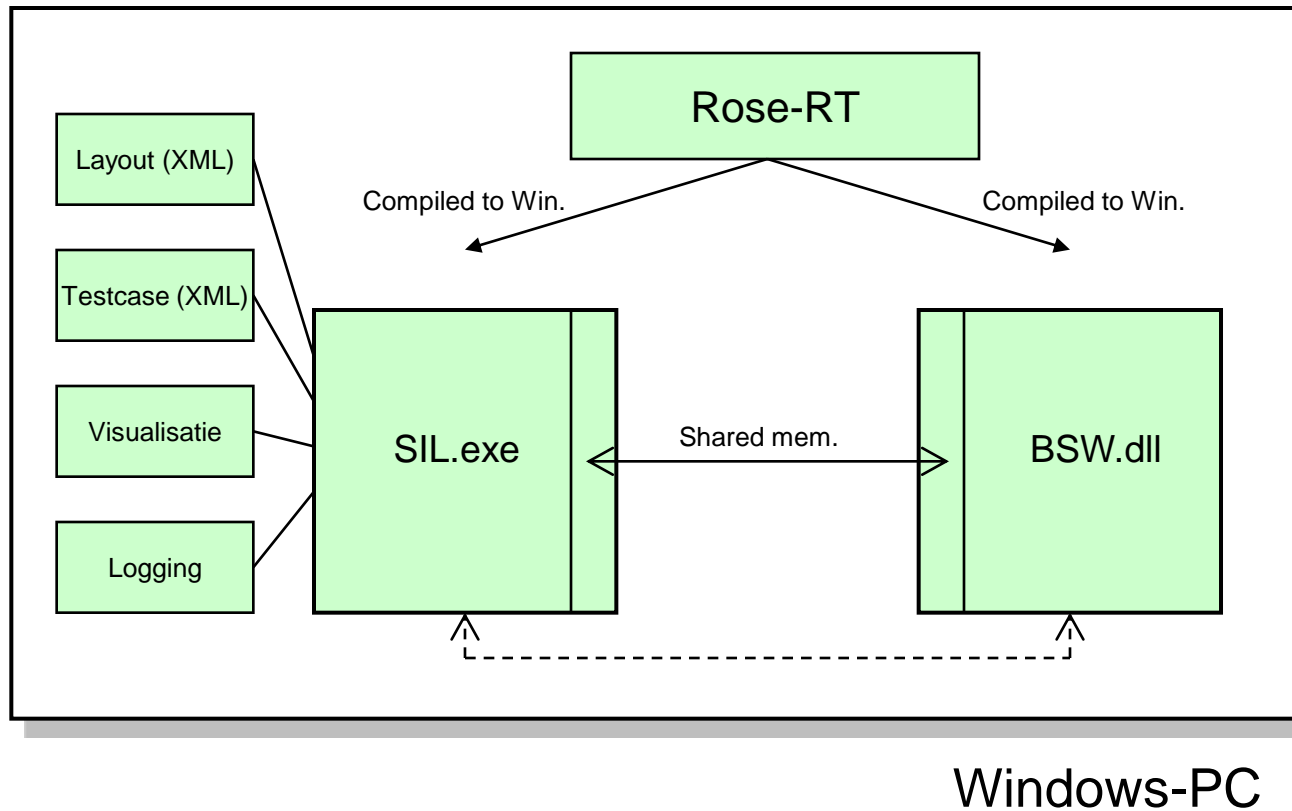


Voordelen van HIL



- ✖ Simuleren van real-time condities
- ✖ Testen van extreme omstandigheden
- ✖ Testen zonder complexe hardware omgeving
- ✖ Beoordelen van alternatieve functionaliteit
- ✖ Fouten injecteren

Oorspronkelijke configuratie (SIL)



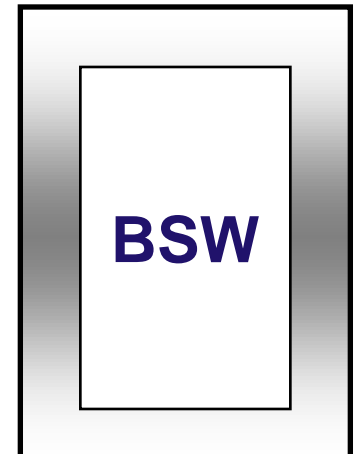
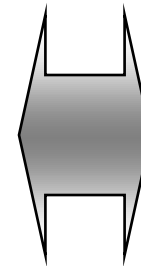
PXI + PC met Windows XP



Windows PC

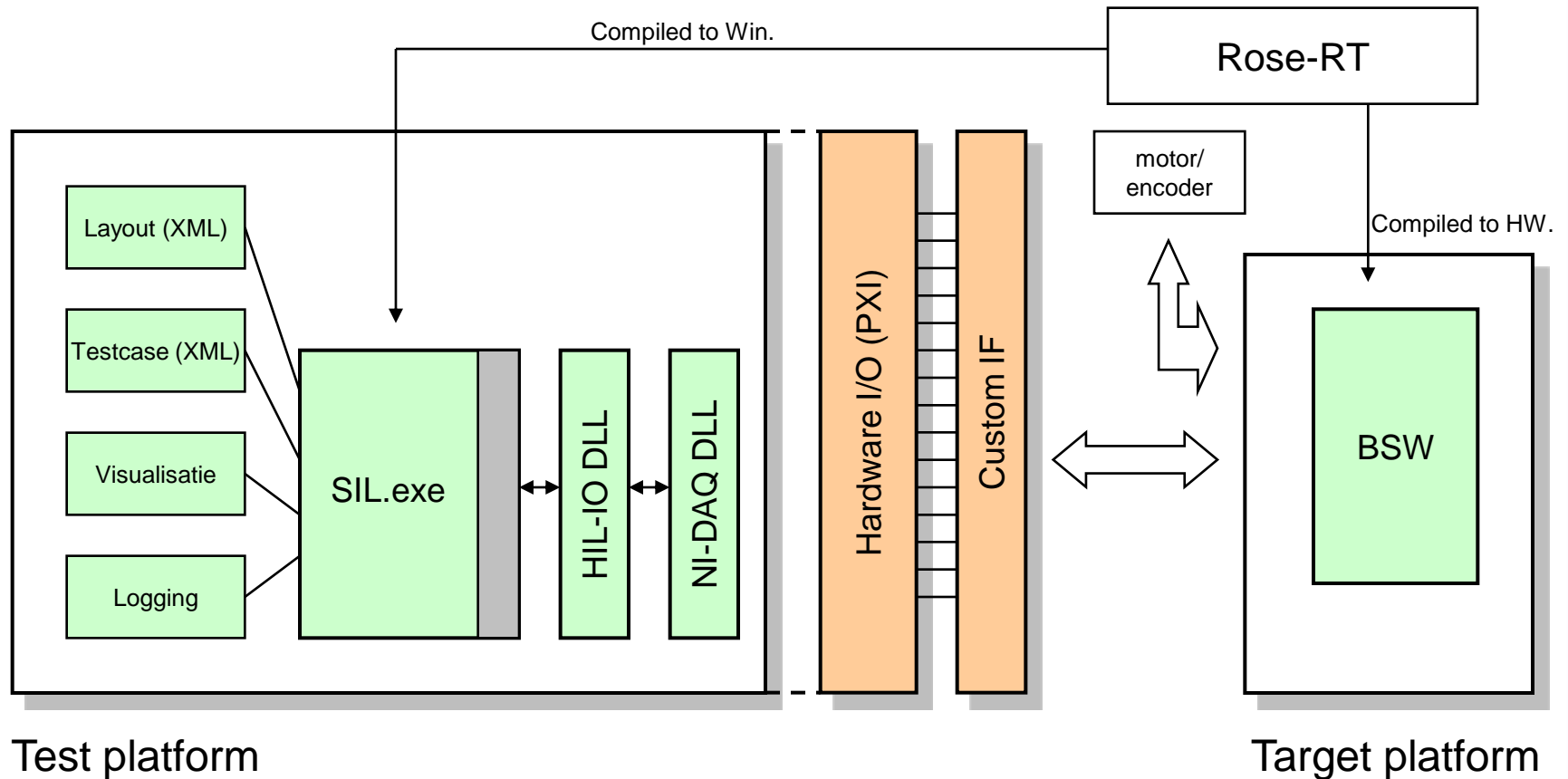
PXI-chassis met I/O

**Device under
Development**



**SIL.EXE
HIL.DLL
NI-DAQ**

Nieuwe configuratie (HIL)

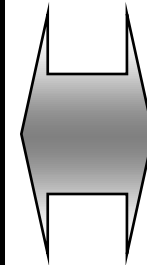
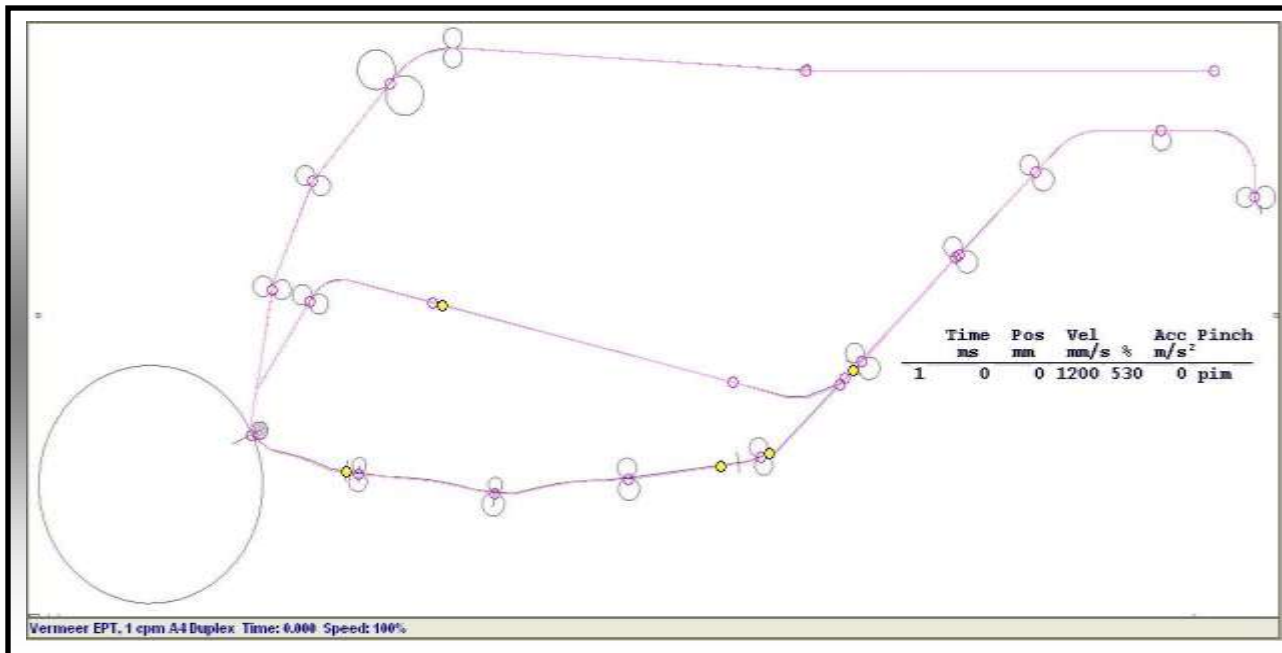


HIL/SIL testopstelling



Besturingsbankopstelling

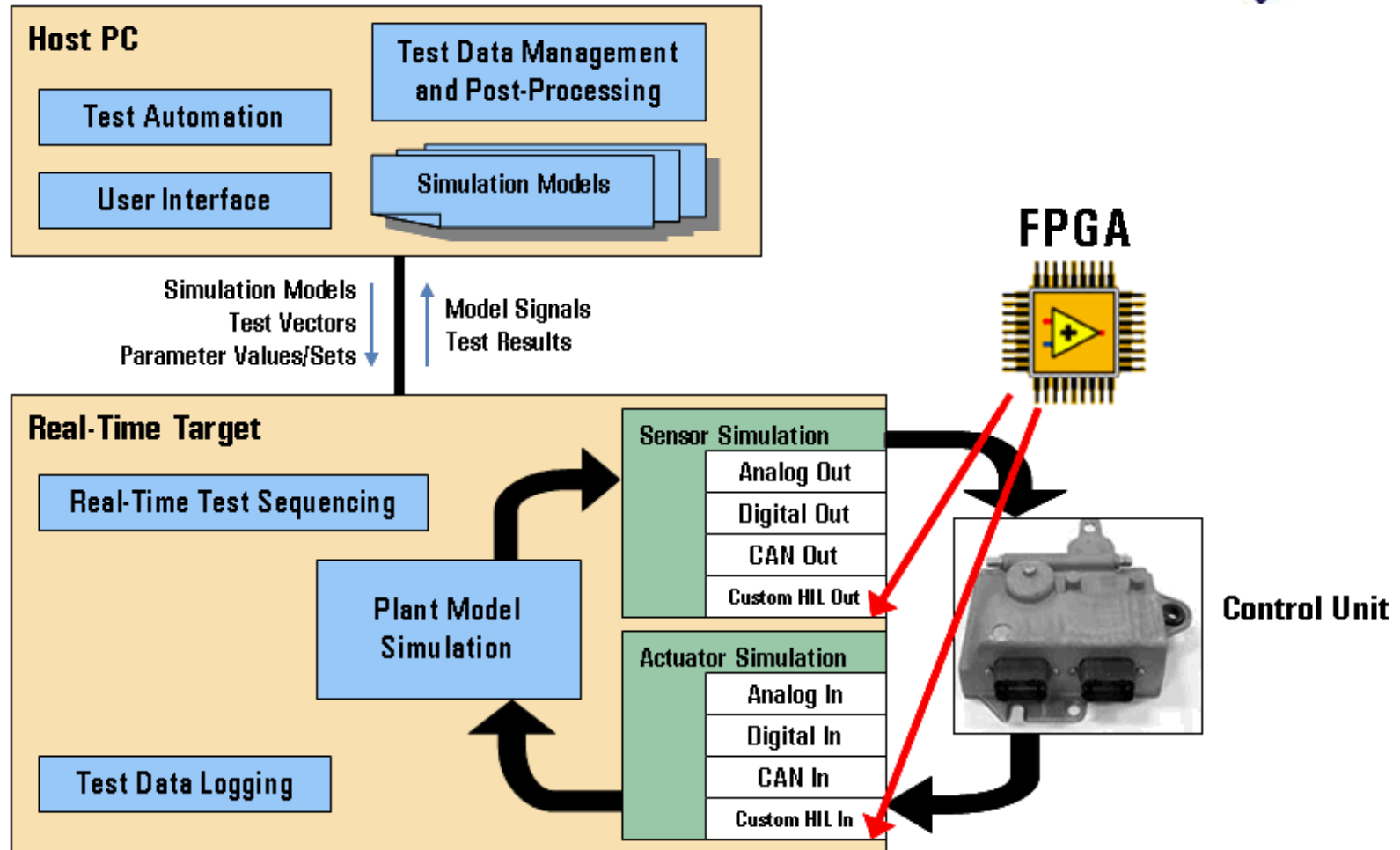
Windows PC



Layout van de complete machine

Testscripts

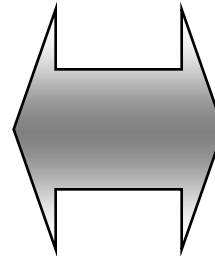
Simulatie architectuur



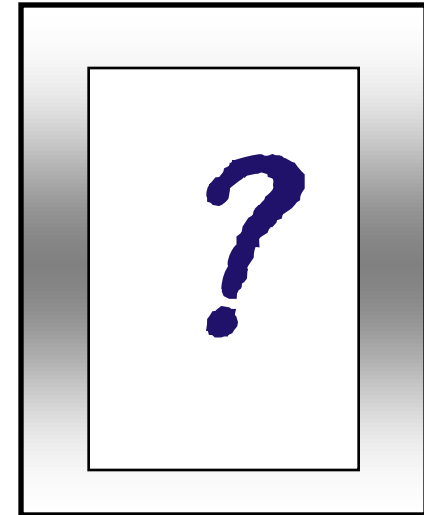
PXI Controller met FPGA



Emulator



**Device under
Development**



**PXI-chassis met RT-Controller
en FPGA
LabVIEW (RT)**