

Product Tier Details

The following lists the schematic capture functionality available in Multisim Student and Education editions:

Functionality	Student	Education
Customizable GUI	X	X
Screen-capture utility	X	X
Comments on schematic	X	X
Circuit annotations	X	X
Modeless part placement and wiring	X	X
Fast retrieval parts bins	X	X
Auto and manual wiring	X	X
Virtual wiring by node name	X	X
Rubber banding on part move	X	X
Fast auto-connect passives	X	X
Subcircuits	X	X
3-dimensional breadboarding	X	X
Virtual NI ELVIS I and II	X	X
Embedded questions—view and respond	X	X
Forward/Back annotation with Ultiboard	X	X
Cross-probing with Ultiboard	X	X
Bus-vector connect		X
Spreadsheet view		X
Design constraints		X
Advanced search		X
Zoom to selected part		X
Corporate database		X
User defined fields		X
Save components to database from workspace		X
Multiple circuits open		X
Embedded questions—create and edit		X
Electrical rules check		X
Graphically mark no-connect pins		X
Hierarchical designs		X
Multisheet designs		X
Project manager		X
Reports—including bill of materials		X

Functionality	Student	Education
Pin and gate swap		X
Export to Mentor PADS layout		X
Device library	Partial	Complete
Maximum components in design	50	Unlimited

Product Tier Details

The following lists the simulation functionality available in Multisim Student and Education editions:

Functionality	Student	Education
Interactive simulation	X	X
Fully mixed-mode A/D simulation	X	X
Standard SPICE 3X5/XSPICE	X	X
Enhanced model support	X	X
Cadence® Pspice® model simulation*	X	X
Speed/Accuracy tradeoffs	X	X
Simulation advisor	X	X
Convergence assistant	X	X
Virtual, interactive, animated parts	X	X
Mouse click support for interactive parts	X	X
Rated components	X	X
Insert faults into components	X	X
Measurement Probes	X	X
Component Wizard	X	X
NI measurement data file sources	X	X
NI measurement data file export	X	X
NI LabVIEW VIs as instruments and sources	X	X
Microphone & speaker	X	X
Circuit restrictions	X	X
Grapher & Postprocessor	X	X
RF design kit	X	X
Circuit wizards		X
C-Code modeling		X

Functionality	Student	Education
Description box synced with simulation		X
Ladder diagrams/components		X
Model makers		X
Load and save simulation profiles		X
Virtual Instruments	22	22
Analyses	10	18
Multisim MCU	X	X
Multisim Automation API		X
* Does not support all Cadence® PSpice® syntax		

Product Tier Details

The following lists the layout functionality available in Ultiboard Student and Education editions:

Functionality	Student	Education
Push and Shove trace placement	X	X
Push and Shove part placement	X	X
Real-time & from copper ratsnest	X	X
Real-time polygon update with voiding	X	X
Forward/Backward annotation	X	X
Cross-probing with Multisim	X	X
Real-time DRC	X	X
64 layers and 1 nanometer resolution	X	X
Comprehensive Footprint Wizard	X	X
Enhanced 3D visualization with print	X	X
User annotations	X	X
Full screen mode		X
Gerber, DXF, IPC-D-356A, SVG output		X
Dimensions on PCB and Landpatterns		X
Dimensions in Database Manager		X
Net bridges		X
3D visualization inside circuit board		X
Turn off ratsnest for selected nets		X

Functionality	Student	Education
Gridless follow-me placement		X
Load and save technology files		X
Polar Grids		X
Customizable layer viewing		X
Split power-planes		X
Keep-in/Keep-out areas		X
Place components in array		X
Unplace all components		X
Ruler bar alignments and measurements		X
Auto-alignment		X
Save PCB Design as a component		X
Permanent grouping		X
Pin & gate swapping		X
Multiple clearances		X
Jump to Error		X
Equispace trace support		X
Differential Impedance Calculator		X
Transmission Line Calculator		X
Microvias		X
Test point insertion		X
Automatic tear-dropping		X
Pin necked trace support		X
Automatic jumper insertion		X
Copy Route & Replica Place functions		X
In-place footprint editor		X
Mechanical CAD		X
Export 3D info in 3D IGES, DXF formats		X
Copper amount report		X
Test point report		X
Customization of report generation		X
Multiple open documents		X
Number of pins supported	350	1,000
Spreadsheet view	Limited	Complete

Product Tier Details

The following lists the autorouting functionality available in Ultiboard Student and Education editions:

Functionality	Student	Education
Fully customizable cost factors	X	X
Progressive Routing	X	X
Interactive autorouting	X	X
Constraint driven routing	X	X
Manual pre-placement: components, vias, traces	X	X
Auto Block Capacitor recognition	X	X
SMD mirroring	X	X
Trace rubberbanding	X	X
Follows keep-in/keep-out criteria	X	X
Pin number limit	350	1,000
Maximum number of layers	2	4