

**Board Assembly Part Number(s)**

Part Number	Description
189718X-0xL including all letter revisions	NI PXI-5610

**Manufacturer:** National Instruments

**Volatile Memory**

Type <sup>1</sup>	Size	User Accessible/ System Accessible <sup>2</sup>	Battery Backup?	Purpose	Method of Clearing <sup>3</sup>
Static Random Access Memory (SRAM)	256 kbit	No/No	No	Not used	Cycle power
FPGA Block RAM	40960 bits	No/No	No	Not used	Cycle power
ASIC	1536 bits	No/No	No	PXI Communication	Cycle power

**Non-Volatile Memory**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
Flash	2 Mbits	No/Yes	No	Stores calibration constants	None available to user
EEPROM	64 kbits	No/No	No	Stores FPGA and MITE configurations	None available to user

**Media Storage**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
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NONE

<sup>1</sup> Calibration constants that are stored in device calibration memory include information for the device's full operating range. Calibration constants do not maintain any unique data for specific configurations at which the device is used unless otherwise specified.

<sup>2</sup> Items are designated **No** for the following reason(s): a) Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed. b) Hardware modifying software tools are not distributed to public users for any personal access or customization; also known as non-normal use.

<sup>3</sup> The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to clear the memory are not distributed by National Instruments to customers for normal use.

**Terms and Definitions**

**User Accessible** User accessible memory allows the user to directly write or modify the contents of the memory during normal instrument operation.

**System Accessible** System accessible memory does not allow the user to access or modify the memory during normal instrument operation, however, may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use. Under normal use conditions user data is not written to any non-volatile memory locations by the system.

**Cycle Power** The process of completely removing power from the device and its components. This includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

**Volatile Memory** Volatile memory requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

**Non-Volatile** Non-volatile memory retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.