

**Board Assembly Part Number(s)**

Description	Part Number
Module Assembly #	157089X-0xL including all letter revisions

**Manufacturer:** National Instruments

**Volatile Memory**

Type <sup>1</sup>	Size	User Accessible/ System Accessible <sup>2</sup>	Battery Backup?	Purpose	Method of Clearing <sup>3</sup>
SRAM	572 Kbits	Yes/No	No	FPGA General Memory	Cycle power
BRAM	512 Kbits	No/No	No	FPGA General Memory	Cycle power
SRAM	128 Mbits	No/No	No	Micro General, Calibration	Cycle power

**Non-Volatile Memory**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
FLASH	16 Mbits	No/No	No	FPGA Enumeration	None available to user
FLASH	64 Mbits	No/No	No	Micro General	None available to user
FLASH <sup>1</sup>	64 Mbits	Yes/No	No	Micro Calibration <sup>1</sup>	None available to user

**Media Storage**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
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NONE

**Clearing Notes:**

The user cannot access the NI PXIe-5654 non-volatile memory; no clearing available.

<sup>1</sup> Calibration constants that are stored in device FLASH components include information for the device's full operating range. Calibration constants do not maintain any unique data for specific configurations at which the device is used unless otherwise specified.

<sup>2</sup> Items are designated **No** for the following reason(s):

- a) Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed.
- b) Hardware-modifying software tools are not distributed to customers for any personal access or customization, also known as non-normal use.

<sup>3</sup> The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to clear the memory are not distributed by National Instruments to customers for normal use.

## Terms and Definitions

**User Accessible** Allows the user to directly write or modify the contents of the memory during normal instrument operation.

**System Accessible** Does not allow the user to access or modify the memory during normal instrument operation. However, system accessible memory may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use.

**Cycle Power** The process of completely removing power from the device and its components. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

**Volatile Memory** Requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

**Non-Volatile** Retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.