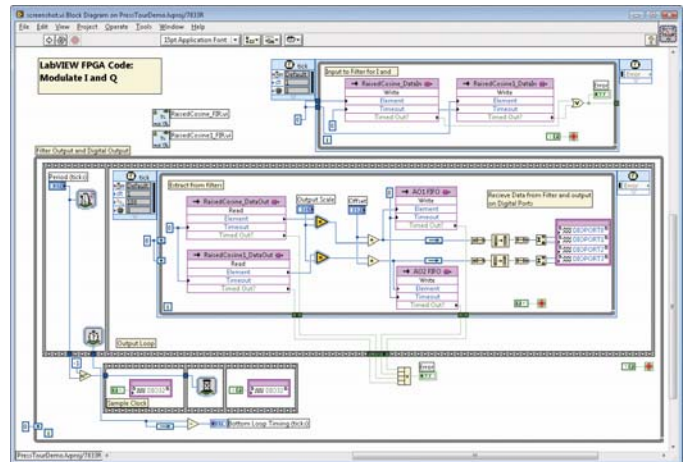


NI LabVIEW 8.5 FPGA Module for Defining Custom I/O

Run LabVIEW Diagrams Directly on Silicon

The National Instruments LabVIEW 8.5 FPGA Module helps you define custom FPGA-based I/O without knowledge of hardware design or VHDL. Using the module, you can easily run LabVIEW diagrams directly on FPGAs. The LabVIEW programming language is especially ideal for FPGA programming because it gives you the ability to visualize and implement parallel programming, pipelining and data flow, which are all important FPGA considerations.

The LabVIEW FPGA Module is a part of the LabVIEW embedded platform – a family of National Instruments products including the LabVIEW Real-Time Module, LabVIEW PDA Module, LabVIEW Touch Panel Module and LabVIEW Microprocessor SDK (Software Development Kit) – that provides a complete software programming environment for embedded systems.



New LabVIEW 8.5 FPGA Module Features

LabVIEW 8.5 FPGA offers a number of new features to help you create FPGA designs more efficiently including:

- Automatic generation of logic and code such as analog and digital measurements with direct memory access (DMA) using a configuration-based FPGA Project Wizard
- High-level design using a statechart approach based on the Unified Modeling Language (UML) specification with the LabVIEW Statechart Module
- New intellectual property (IP) for multichannel operations including PID control for more efficient FPGA gate usage
- Notch filters you can tune at run time for enhanced signal processing and control
- Signal generators including noise patterns for sensor simulation and arbitrary waveform generation
- An online directory of FPGA functions and examples from NI and the LabVIEW FPGA community at www.ni.com/ipnet
- Simulation of FPGA functions on windows before compiling for FPGA targets to speed design
- Improved pipelining support to deliver greater system performance

For more information on the LabVIEW 8.5 FPGA Module, visit www.ni.com/fpga.

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